

## CLAIMS

What is claimed is:

- 1        1.    A computing device, comprising:  
2        a chassis that contains information processing logic;  
3        and  
4        a display panel rotatably coupled to the chassis, the  
5        display panel including a housing to contain a display  
6        screen and a plurality of antennas each operating at a  
7        different center frequency.
- 1        2.    The computing device of claim 1, wherein each of  
2        the plurality of antennas is coupled to a dedicated front-  
3        end.
- 1        3.    The computing device of claim 2, wherein the  
2        dedicated front-ends associated with the plurality of  
3        antennas is contained within the housing.
- 1        4.    The computing device of claim 2, wherein the  
2        corresponding plurality of front-ends are coupled to the  
3        information processing logic of the chassis through a link.
- 1        5.    The computing device of claim 1, wherein the  
2        chassis includes a top surface integrated with an  
3        alphanumeric keyboard.
- 1        6.    The computing device of claim 1, wherein the  
2        plurality of antennas contained in the housing of the  
3        display panel enable service to a wireless local area  
4        network operating in accordance with an IEEE 802.11 standard

5 and at least one of a Bluetooth based network and a  
6 HiperLAN/x based network.

1 7. The computing device of claim 6, wherein the  
2 plurality of antennas contained in the housing of the  
3 display panel further enable service to a global positioning  
4 system.

1 8. The computing device of claim 1, wherein the  
2 plurality of antennas contained in the housing of the  
3 display panel enable service to a Bluetooth based network  
4 and at least one of a global positioning system and a  
5 HiperLAN/x based network.

1 9. The computing device of claim 1, wherein the  
2 plurality of antennas contained in the housing of the  
3 display panel enable service to at least two of a wireless  
4 local area network operating in accordance with an IEEE  
5 802.11 standard, a global positioning system, a Bluetooth  
6 based network and a HiperLAN/x based network.

1 10. The computing device of claim 1, wherein the  
2 plurality of antennas receive RF signals having different  
3 center frequencies.

1 11. Configured for a computing device, a display panel  
2 comprising:  
3 a housing;  
4 a display screen having a backside partially protected  
5 by the housing; and

6 a plurality of antennas placed within the housing, each  
7 of the plurality of antennas operating at a different center  
8 frequency.

1 12. The display panel of claim 11, wherein each of the  
2 plurality of antennas is coupled to a dedicated front-end.

1 13. The display panel of claim 11, wherein the display  
2 screen is a liquid crystal display (LCD) screen.

1 14. The display panel of claim 11, wherein the  
2 plurality of antennas enable service to at least two of a  
3 wireless local area network, a global positioning system, a  
4 high performance radio local area network, and a Bluetooth  
5 based network.

1 15. A computing device comprising:  
2 a chassis to protect logic including a processor and a  
3 chipset coupled to the processor; and  
4 a display panel including a housing partially  
5 surrounding a display screen and substantially containing a  
6 plurality of antennas each operating at a different center  
7 frequency, the display panel in communication with the logic  
8 protected by the housing.

1 16. The computing device of claim 15, wherein each of  
2 the plurality of antennas is coupled to a unique front-end,  
3 each front-end being coupled to a link.

1 17. The computing device of claim 16, wherein the link  
2 is coupled to an accelerated graphics port of the chipset  
3 employed within the chassis.

1        18. The computing device of claim 16, wherein the link  
2 is a digital visual interface (DVI) cable.

1        19. The computing device of claim 18, wherein the DVI  
2 cable is coupled to a graphics controller being part of the  
3 logic implemented within the chassis.

1        20. The computing device of claim 19, wherein the  
2 graphics controller is coupled to a Transmission Minimized  
3 Differential Signaling (TMDS) transmitter.

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